

Precision Physical

Precision Physical features post-place-and-route physical synthesis to meet aggressive timing requirements for complex FPGA designs.

In addition to post-P&R physical synthesis, Precision Physical offers an interactive layout editor for further physical optimizations and a powerful placement reuse flow for incremental changes. Precision Physical is a super-set of Precision RTL and Precision RTL Plus.

KEY FEATURES & BENEFITS

Physical Synthesis

- Performs optimizations using post-P&R data
- Reduces runtime of future P&R iterations

Interactive Physical Layout Editor

- Find trouble areas in physical implementation
- Optimize specific areas or timing paths

Modular Design Reuse Flow

- Reuses placement of unmodified blocks

Enhanced Design Analysis

- Cross-probe between design schematics and physical layout

All Features of Precision RTL Plus

- Mil-aero & safety-critical features
- Physical synthesis (pre-P&R)
- Low power synthesis
- Incremental flows
- Precise-IP™
- Precise-Encrypt
- Mixed language support with SystemVerilog
- Resource Manager
- ASIC prototyping support
- Integration with Mentor tools
- Vendor independent FPGA debug and validation
- Seamless push-button instrumentation without any HDL modification

- System speed FPGA validation with virtually unlimited visibility

Precise-Encrypt

- Vendor independent HDL encryption
- Based on IEEE P1735 draft
- Interoperable with ModelSim & Questa

Integration with Mentor Tools

- Design reuse with HDL Designer™
- Equivalence checking with FormalPro™
- Requirements tracking with ReqTracer™
- FPGA-PCB co-design with I/O Designer

Resource Manager

- Graphical analysis of embedded resources
- Aids in improving performance and area

All Precision RTL Features

- Award-winning analysis
- Mixed language support with SystemVerilog
- ASIC prototyping support
- Precise™-IP